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| Class | Subclass |
| 710 | 2111 |

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PATENT NUMBER

U.S. UTILITY Patent Application

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| SCANNED: 7/5/2 O.A. SS | PATENT DATE |
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| APPLICATION NO. 09/899436 | CONT/PRIOR D | CLASS 710 | SUBCLASS 2111 | ART UNIT 2111 | EXAMINER WONGT AVE |
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APPLICANTS
Harry Chue
Garry Tsang

TITLE
System and method for efficiently implementing an electronic device architecture

ISSUING CLASSIFICATION

| ORIGINAL | | CROSS REFERENCE(S) | | | |
|------------------------------|----------|--------------------|-----------------------------------|--|--|
| CLASS | SUBCLASS | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | |
| INTERNATIONAL CLASSIFICATION | | | | | |
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| <input type="checkbox"/> TERMINAL DISCLAIMER <input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| | | | | NOTICE OF ALLOWANCE MAILED | |
| | | | | ISSUE FEE | |
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